

T.P.C.T's
COLLEGE OF ENGINEERING, OSMANABAD
DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION
TIME TABLE 2017-18 (Sem -I)

BE(ECT-A)
Hall No. 51

w.e.f.03/07/2017

Day/ Time	9.30 To 10.30	10.30 To 11.30	11.30 To 12.30	12.30 To 1.20	1.20 To 2.20	2.20 To 3.20	3.20 To 3.30	3.30 TO 5.30					
								DIP	ES	MWRE	VLSI	EL-I	PP-I
Mon	ES	VLSI Design	DIP	B	MWRE	EL-I	B		BA1 (MP LAB)	BA2 (MW LAB)	BA3 (VLSI LAB)		
Tue	VLSI Design	EL-I	CS&QA	R	DIP	ES	R	BA1 (CC LAB)	BA2 (MP LAB)	BA3 (MW LAB)			
Wed	MWRE	EL-I	CS&QA	E	VLSI Design	ES	E		BA3 (MP LAB)		BA2 (VLSI LAB)		BA1 (PROJ LAB)
Thu	CS&QA	VLSI Design	DIP	A	ES	MWRE	A	BA2 (CC LAB)		BA1 (MW LAB)		BA3 (CE LAB)	
Fri	EL-I	DIP	MWRE	K	BA1-VLSI-NVB-VLSI LAB BA2-EL-I-RPS-CE LAB- BA3-PPI-PSK-PROJ LAB		K	BA3 (CC LAB)				BA1 (CE LAB)	BA2 (PROJ LAB)

DIP
Dr.S.S.Kanade
ES
Prof.L.M.Deshpande
MWRE
Prof.P.S.Kolhe
VLSI Design
Prof.Mrs.N.V.Bhosale
EL-I
Prof.R.P.Shelkikar.
CS&QA
Prof.A.P.Mane

CC
Prof.R.P.Shelkikar.

Prof.S.G.Aghor
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Dr. S. S. Kanade
H.O.D.(ECT)

Prof.A.B.Ghalke
Dean Academic

Dr.N.S.Biradar
PRINCIPAL