

**T.P.C.T's**  
**COLLEGE OF ENGINEERING, OSMANABAD**  
**DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION**  
**TIME TABLE 2017-18 (Sem -I)**

**BE(ECT-B)**  
**Hall No. 52**

w.e.f.03/07/2017

Day/ Time	9.30 To 10.30	10.30 To 11.30	11.30 To 12.30	12.30 To 1.20	1.20 To 2.20	2.20 To 3.20	3.20 To 3.30	3.30 TO 5.30					
								DIP	ES	MWRE	VLSI	EL-I	PP-I
<b>Mon</b>	MWRE	DIP	EL-I	<b>B</b>	ES	VLSI Design	<b>B</b>	BB1 (CC LAB)				BB2 (CE LAB)	
<b>Tue</b>	MWRE	ES	VLSI Design	<b>R</b>	EL-I	CS&QA	<b>R</b>				BB2 (VLSI LAB)	BB1 (CE LAB)	
<b>Wed</b>	VLSI Design	EL-I	ES	<b>E</b>	MWRE	DIP	<b>E</b>	BB2 (CC LAB)		BB1 (MW LAB)			
<b>Thu</b>	DIP	VLSI Design	EL-I	<b>A</b>	MWRE	CS&QA	<b>A</b>		BB1 (MP LAB)				BB2 (PROJ LAB)
<b>Fri</b>	DIP	ES	CS&QA	<b>K</b>	BB1-PP-I-PSK-PROJ LAB BB2-MWRE-APM-MW LAB		<b>K</b>		BB2 (MP LAB)		BB1 (VLSI LAB)		

**DIP**  
**Prof.S.G.Shinde**  
**ES**  
**Prof.L.M.Deshpande**  
**Prof.Mrs.N.V.Bhosale**  
**MWRE**  
**Prof.A.P.Mane**  
**VLSI Design**  
**Prof.P.B.Shitole**  
**EL-I**  
**Prof.V.A.Daware**  
**Prof.Mrs.V.C.Mainger**  
**gi**  
**CS&QA**  
**Prof.R.P.Shelkikar.**  
  
**CC**  
**Prof.S.G.Shinde**

**Prof.S.G.Aghor**  
**TTC Co ordinator**

**Dr. S. S. Kanade**  
**H.O.D.(ECT)**

**Prof.A.B.Ghalke**  
**Dean Academic**

**Dr.N.S.Biradar**  
**PRINCIPAL**